

REMARKS

Reconsideration of this application is respectfully requested in view of the foregoing amendments and the following remarks.

As a preliminary matter, Applicants appreciate the indication of allowable subject matter in claims 1-8 of the present application.

By the foregoing Amendment, the specification has been amended. Claims 1-8 have been amended, without narrowing the scope of the claims. Claims 1-8 have been amended for grammatical and clarity purposes. No new matter has been added. Thus, Applicants respectfully submit that the present application including claims 1-8 are in compliance with U.S. patent practice. Therefore claims 1-8 are in condition for allowance, and Applicants respectfully request that this application be passed to issuance.

In the Office Action mailed May 20, 2004, the Examiner objected to the drawings as not showing "the analog switch including a circuit for setting a potential of a gate of the first p-channel MOSFET to a potential of negative power supply voltage or ground potential; and for setting the potential of the gate to a potential lower than the potential of the negative power supply voltage or the ground potential," as recited in claims 5-8.

Applicants respectfully traverse the objection to the drawings, and respectfully submit that the subject matter recited in claims 5-8 of the present application are indeed shown in drawings as originally filed. In particular, Applicants highlights Figure 1 of the drawings showing an analog switch circuit 1 having a voltage boost circuit 300. In addition, Figure 1 shows an analog switch section 100 having a NMOS transistor NM1 and a PMOS transistor PM1. Hence, Figure 1 clearly illustrates the subject matter of

"the analog switch including a circuit for setting a potential of a gate of the first p-channel MOSFET to a potential of negative power supply voltage or ground potential, and for setting the potential of the gate to a potential lower than the potential of the negative power supply voltage or the ground potential" as recited in claims 5-8 of the present application.

Applicants further highlight page 16, lines 1-11 of the present specification as originally filed. In particular, the foregoing highlighted section discloses,

However, it is a matter of course that a circuit for making the potential of the gate of the PMOS transistor PM1 lower than GND can be used in the same way of thinking. In this case, the voltage boost circuit 300 shown in Fig. 1 makes the potential of the gate of the PMOS transistor PM1 negative power supply potential or ground potential if input potential V_i is higher than reference potential. The voltage boost circuit 300 makes the potential of the gate of the PMOS transistor PM1 lower than negative power supply potential or ground potential if input potential V_i is lower than reference potential. (As amended).

In view of the above, Applicants respectfully submit that the drawings of the present application as originally filed show all the features recited in the claims, and therefore respectfully request that the objection to the drawings to be withdrawn.

The Examiner objected to the specification as containing some minor informalities. The specification has been amended responsive to the objection. Withdrawal of the objection is therefore requested. If any additional amendment is necessary to overcome this objection, the Examiner is requested to contact the Applicants' undersigned representative.

The Examiner also objected to claims 1-8 as containing some minor informalities. Claims 1-8 have been amended responsive to the objection, without narrowing the

scope of the claims. Withdrawal of the objection is therefore requested. If any additional amendment is necessary to overcome this objection, the Examiner is requested to contact the Applicants' undersigned representative.

For all of the above reasons, it is respectfully submitted that the claims now pending are in condition for allowance and in compliance with U.S. patent practice. Accordingly, reconsideration and withdrawal of the outstanding objections and an issuance of a Notice of Allowance are earnestly solicited.

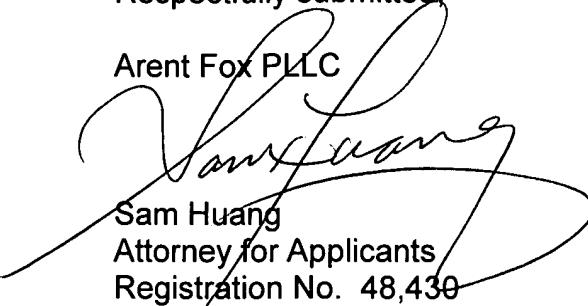
Should the Examiner determine that any further action is necessary to place this application in better form, the Examiner is encouraged to telephone the undersigned representative at the number listed below.

In the event this paper is not considered to be timely filed, the Applicants hereby petition for an appropriate extension of time. The fee for this extension may be charged to our Deposit Account No. 01-2300, referring to client-matter number 107337-00049.

The Commissioner is hereby authorized to charge any fee deficiency or credit any overpayment associated with this communication to Deposit Account No. 01-2300, referring to client-matter number 107337-00049.

Respectfully submitted,

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Enclosures: Petition for Extension for Time (1 month)

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